

The SD-8510 Unveiled

The Meeting

The auditorium's fluorescent lights buzzed as Dr. Issac Korr tapped the podium mic. Behind him, a tarp-covered object hummed faintly. Too faintly, thought Halberg.

"Colleagues," Izzy began, clearing his throat with deliberate ceremony, "I give you the next step in synthetic-computational evolution. The KorrTronix-9001. It's over 9,000."

He whipped away the tarp. Beneath it lay a compact board studded with computer chips and a tiny glowing shard of what appeared to be a quartz crystal that glimmered like frozen lightning.

Before anyone could speak, Dr. Magnusberg leaned forward, tenting his fingers. "Korr. The name." "Yes, yes, something institutional. I think we agreed on.. Stellar Dynamics 8510? After all, they provided the sample. I propose the SD-8510."

The room murmured approval. Korr shrugged. "Fine, fine, SD-8510. As I was saying—this processor integrates discoveries derived directly from Sample SD-0064. Observe."

A holo-projector lit up with various unintelligible diagrams. "We've implemented alternate-dimension registers," Izzy said, beaming. "Instead of saving state on a local "stack", the CPU offloads register contents into temporally adjacent universes. Context switching becomes instantaneous. The registers return through a quantum portal with—well, slight warmth."

Halberg shot to his feet. "Slight warmth? Izzy, that's a miniature resonance cascade! Don't you remember what happened last time? And what about zero-page technology? You're using it again. Teleporting bits around is dangerous. We can't predict the side effects!"

Unforeseen Consequences

Izzy waved a hand. "Oh yes, bit teleportation technology! It works using Quantum Entanglement. The resonance-cascade heatmaps are perfectly balanced. As long as we keep the portal coefficients below 0.9, it operates at peak performance. Using embedded micro-crystal replicas, individual bits can relocate anywhere within the address space without traveling through it. It's... beautiful."

The projector flickered.

Somewhere deep in the facility, a low rumble pulsed—like a giant exhaling.

Izzy paused only a moment. "Of course, we'll add a processing-speed limiter. If the chip runs too fast, the portals may... coalesce. Into a minor resonance cascade. But this is a manageable contingency. Completely routine."

The lights dimmed. "It's just a power surge, everyone remain calm!" Korr announced. "At least, I think it's just a power surge." No one breathed.

The lights rose up to normal and everyone breathed.

STELLAR DYNAMICS INTERNAL MEMORANDUM

Classification: EYES ONLY – EPSILON CONTAINMENT FACILITY

From: Dr. I. Korr

To: Facility Director Dr. Magnusberg

Date: 1983-06-03

Subject: SD-8510 Internal Memo – REDACTED

Observed anomalies during stress test:

- Register “shadow” pair exhibits non-deterministic values after >1.023 MHz
- Zero-page transactions recorded negative 3.7 ns latency (data arriving before request)
- Technician Morales claims to have heard “distant typing” from powered-down unit #004

Until portal stabilization array is redesigned, do not engage Turbo mode operate above 1 MHz. I recommend we halt production of the SD-8510 wafers until we can stabilize the new samples.

[REDACTED] stamps in red: APPROVED FOR LIMITED PRODUCTION – MARKETING

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